

BB-88: Clock and Reset Logic

TITLE

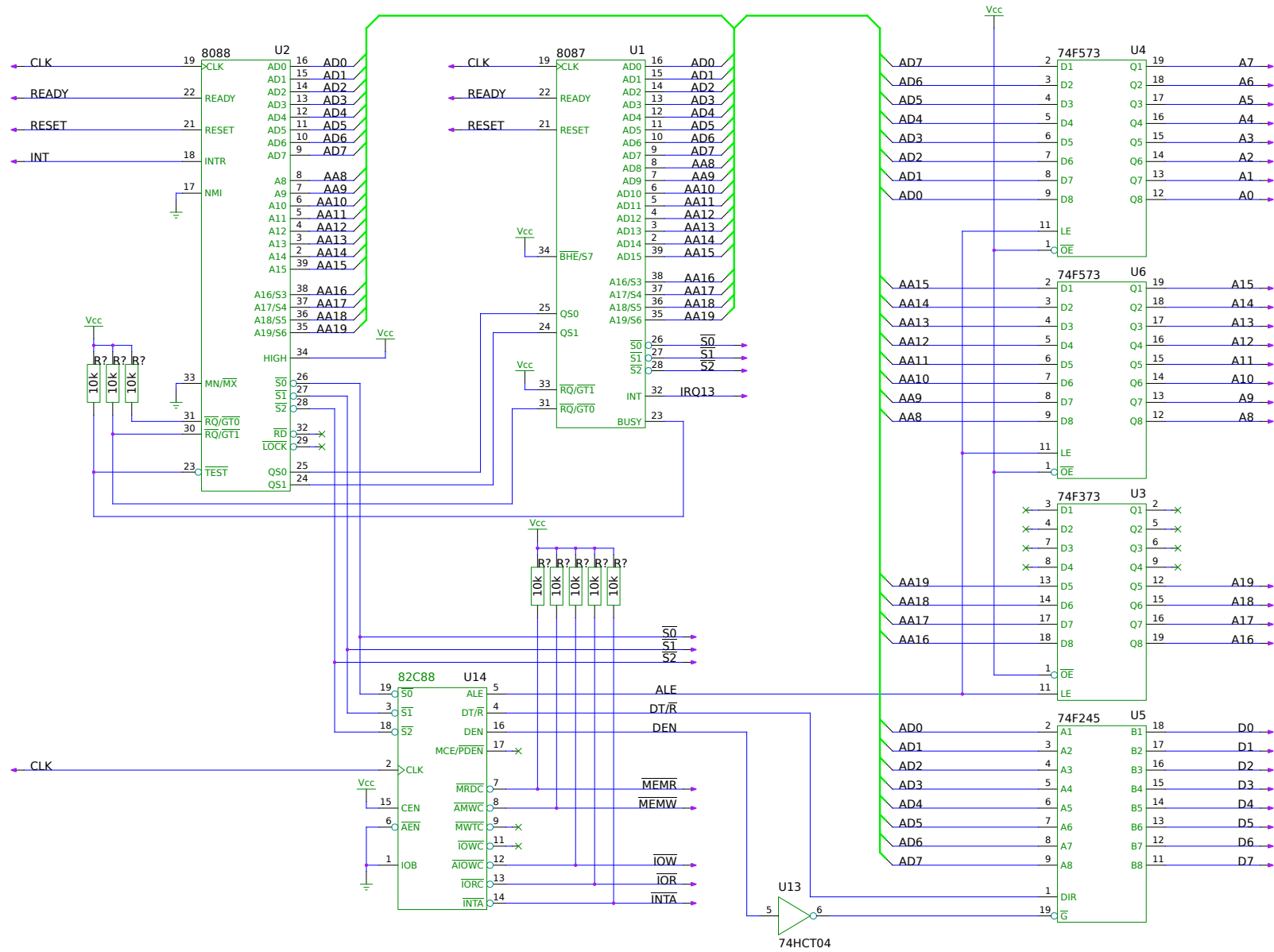
FILE: BB-88_page01.sch

DATE: 26 apr 2021

REVISION: 1.1

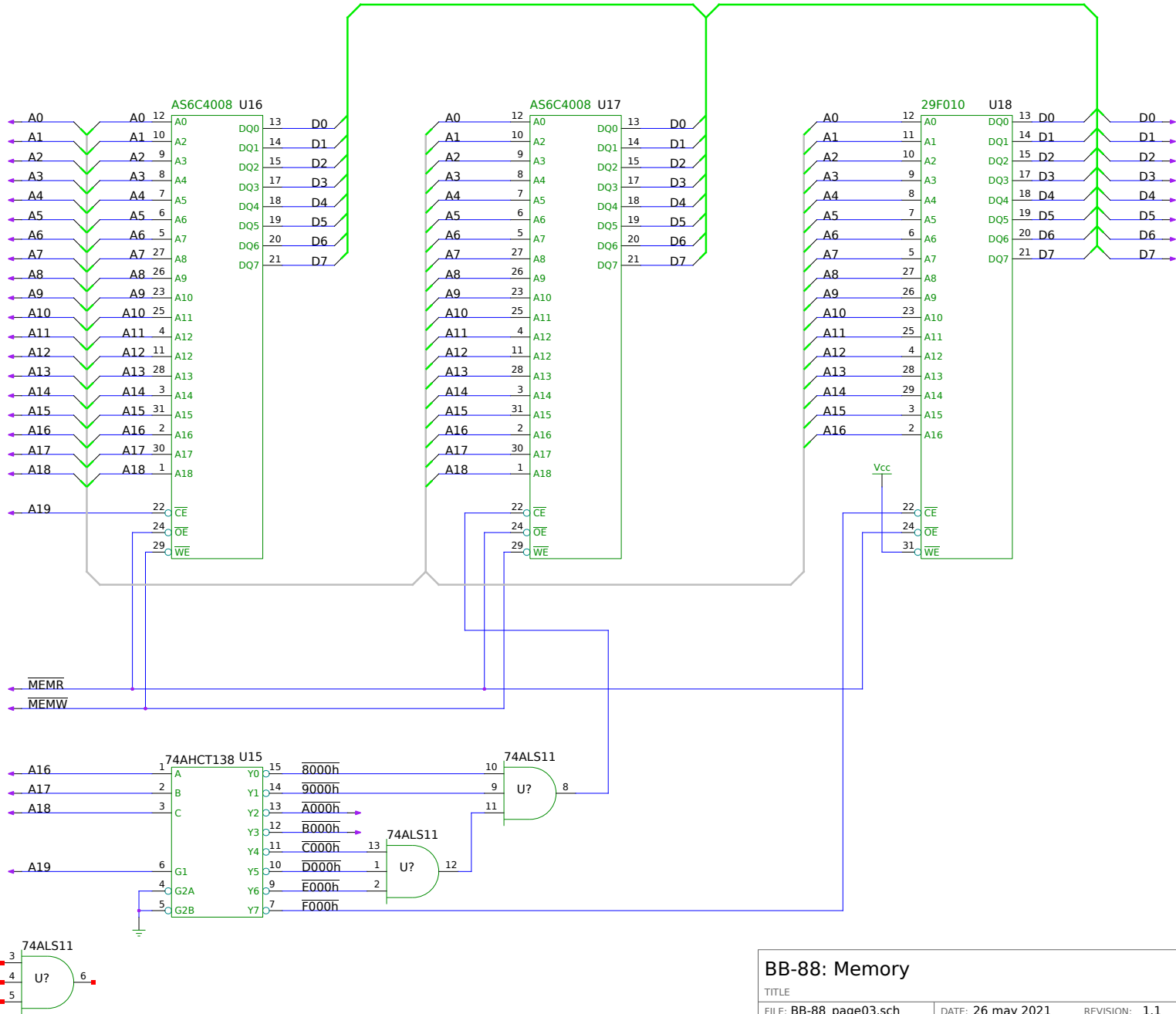
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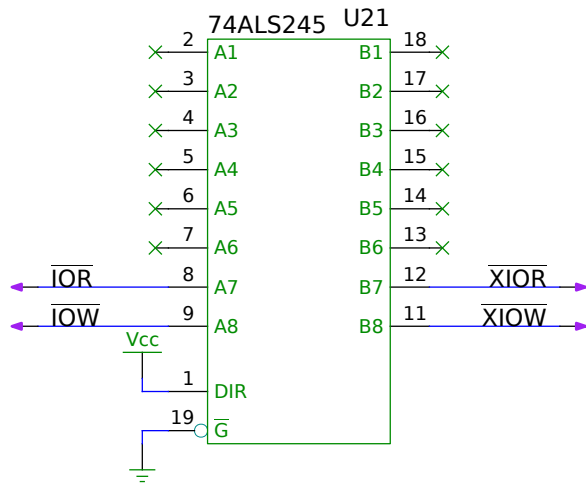
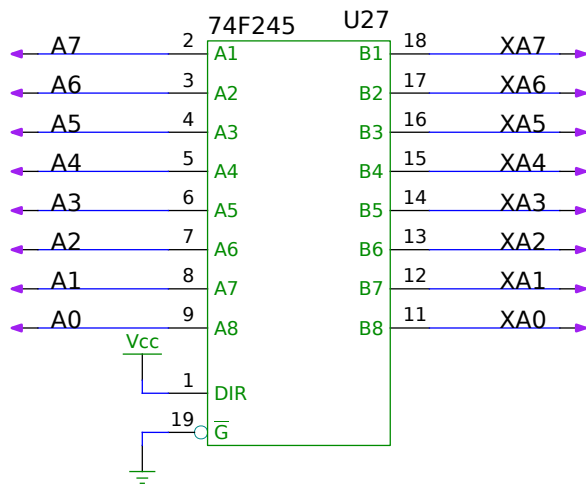
BB-88: Processor and Bus Drivers

TITLE		
FILE: BB-88_page02.sch	DATE: 26 apr 2021	REVISION: 1.0
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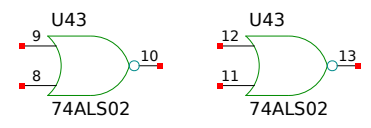
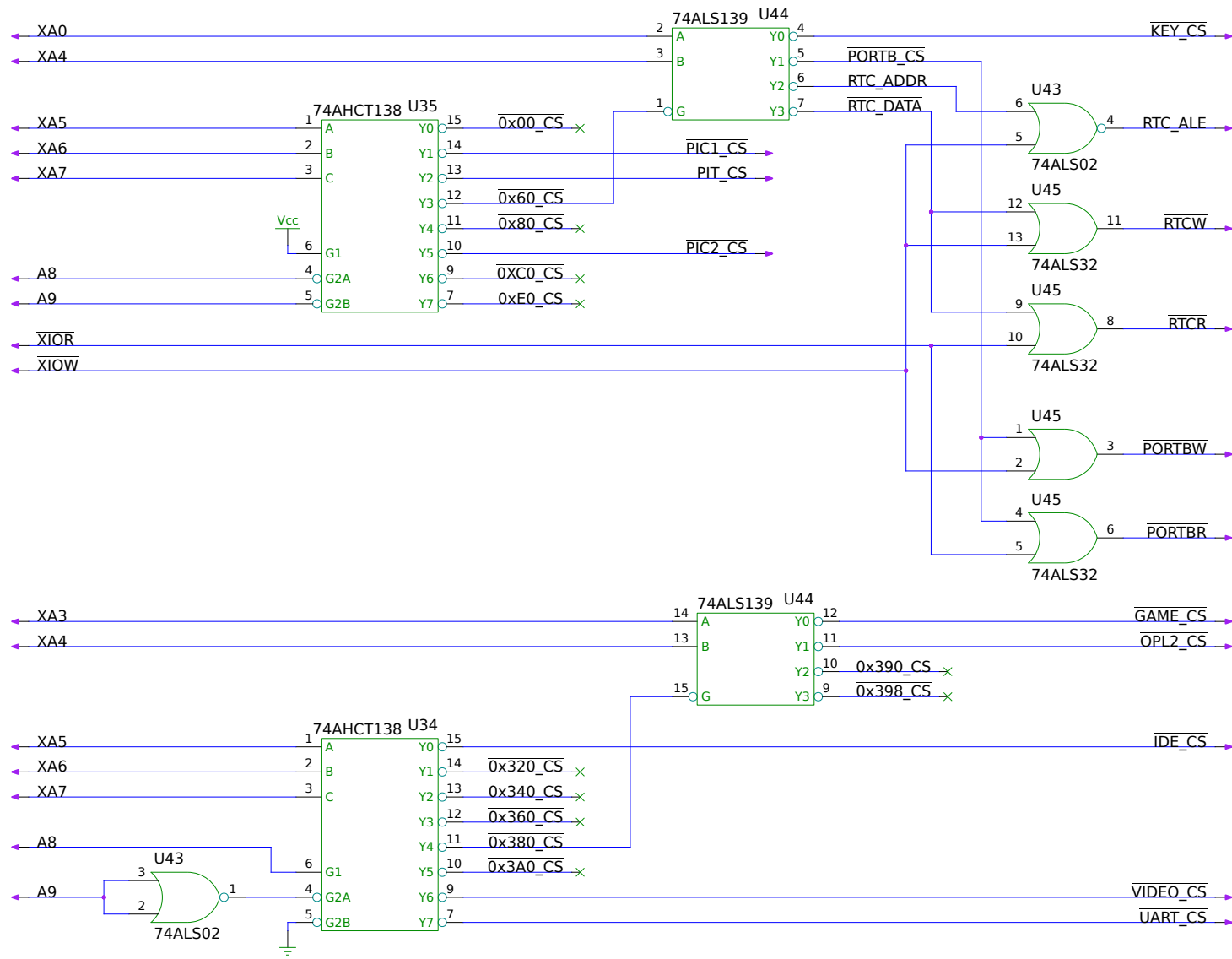
BB-88: Memory

TITLE		
FILE: BB-88_page03.sch	DATE: 26 may 2021	REVISION: 1.1
PAGE 03	OF 22	DRAWN BY: Amand Tihon

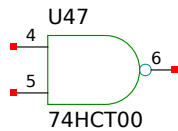
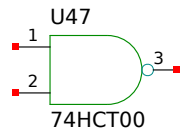
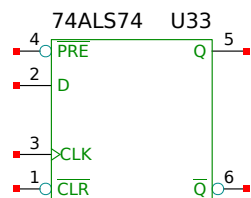
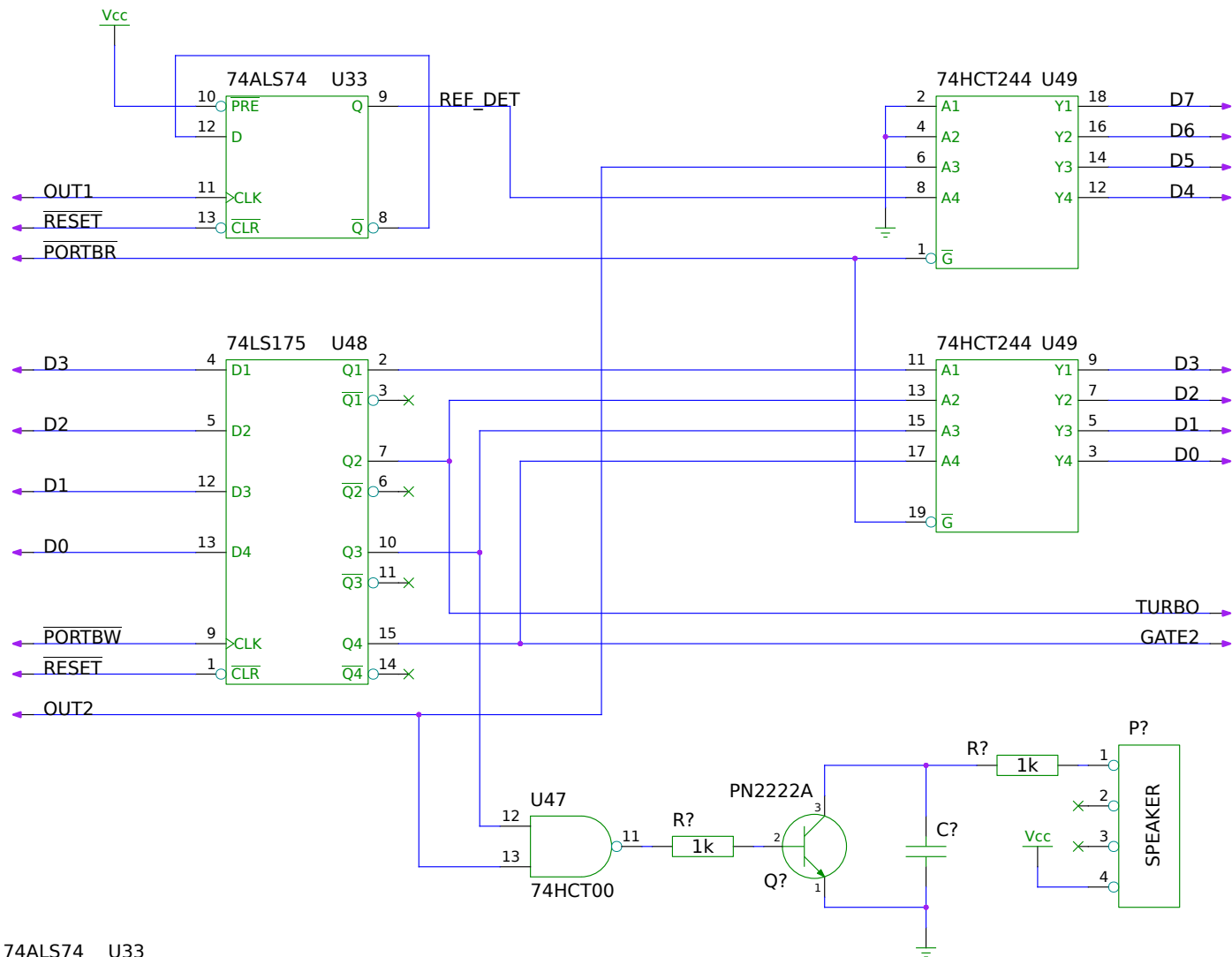


Note:
 These buffers have been added while debugging some signal integrity issues and have never been removed. It is not known whether they are actually needed, but they don't hurt.

BB-88: Bus Buffers		
TITLE		
FILE: BB-88_page04.sch	DATE: 26 apr 2021	REVISION: 1.0
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BB-88: Base IO Selectors		
TITLE		
FILE: BB-88_page05.sch	DATE: 26 apr 2021	REVISION: 1.0
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BB-88: Port B (61h) and Speaker

TITLE

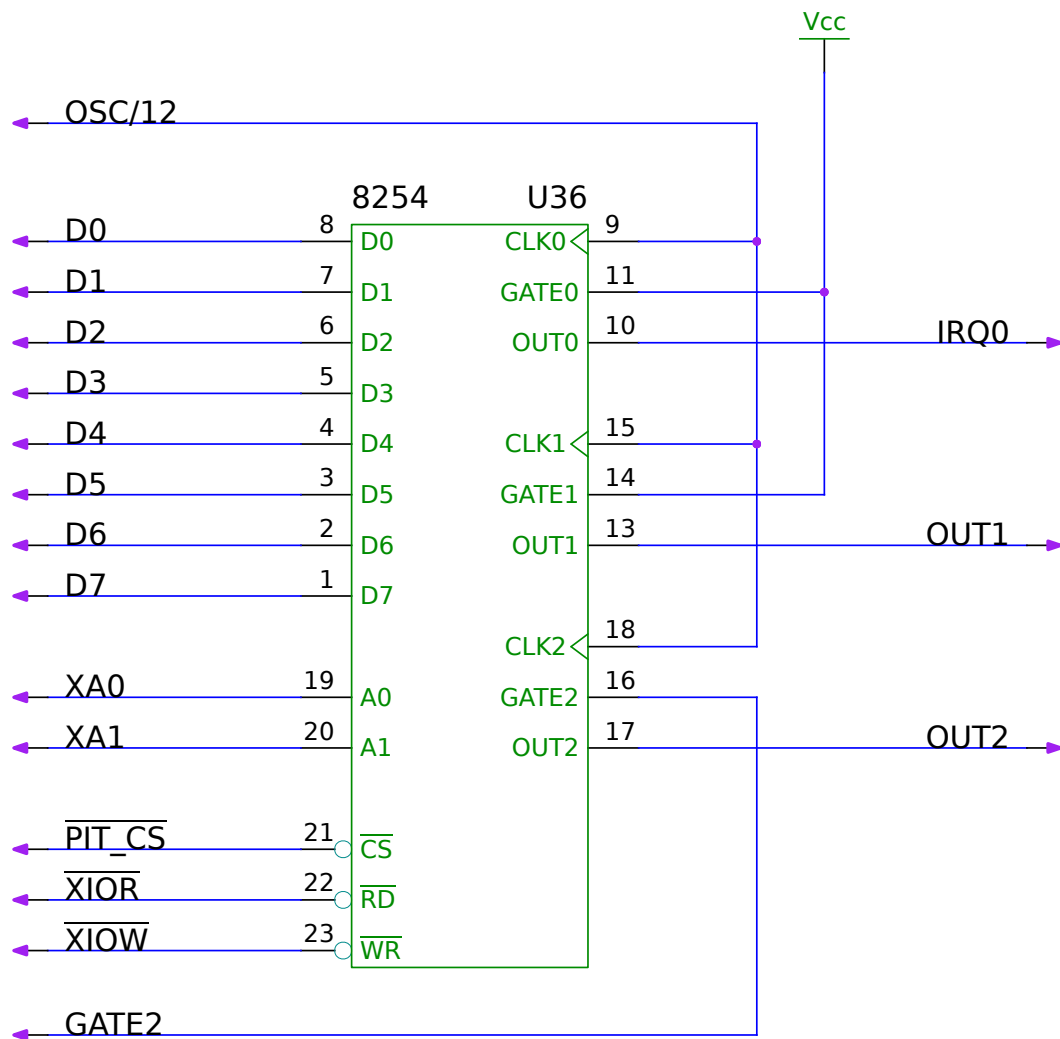
FILE: BB-88_page06.sch

DATE: 26 apr 2021

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BB-88: Timer (PIT)

TITLE

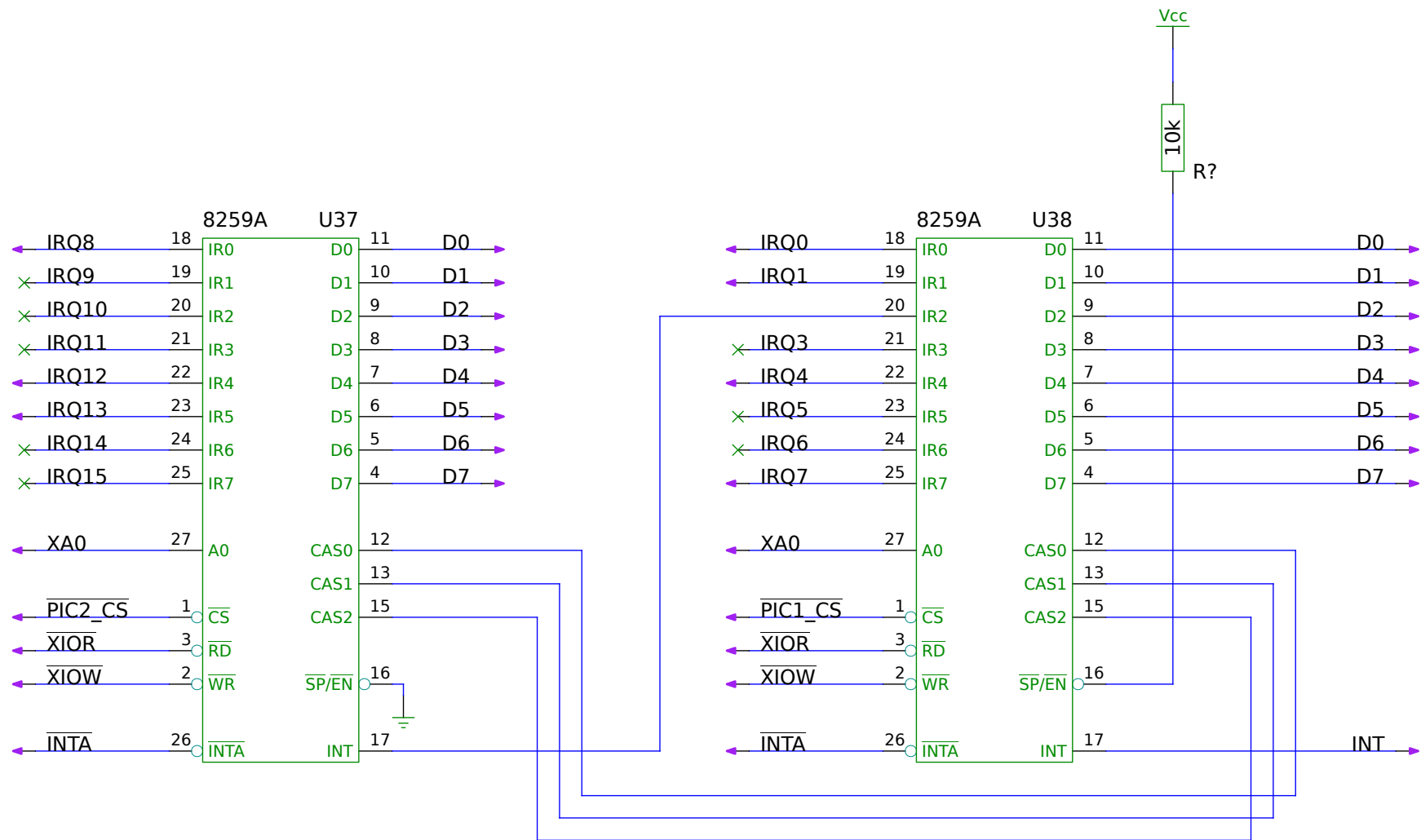
FILE: BB-88_page07.sch

DATE: 26 apr 2021

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BB-88: Interrupt Controllers

TITLE

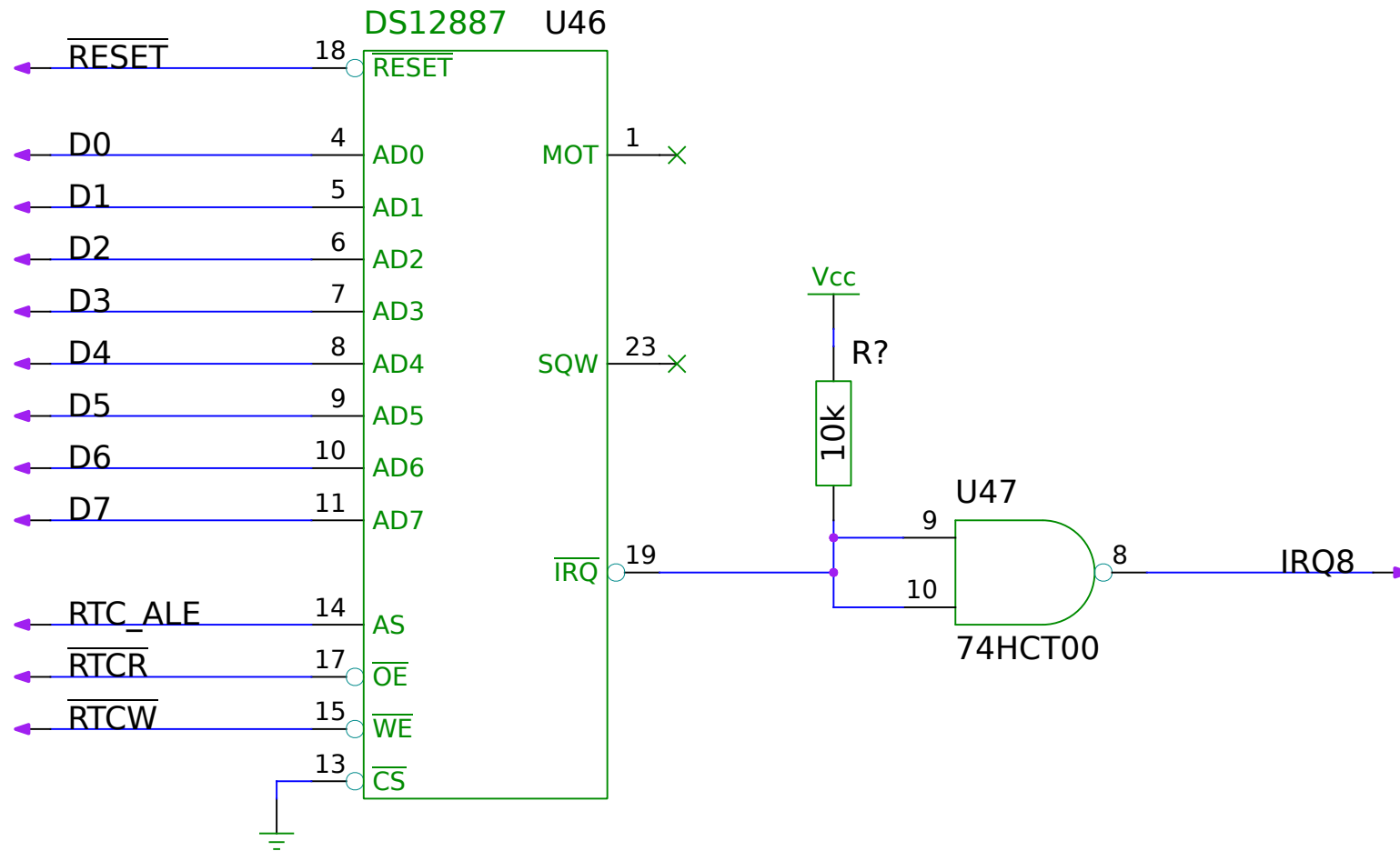
FILE: BB-88_page08.sch

DATE: 26 apr 2021

REVISION: 1.0

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BB-88: Real Time Clock (RTC)

TITLE

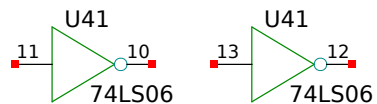
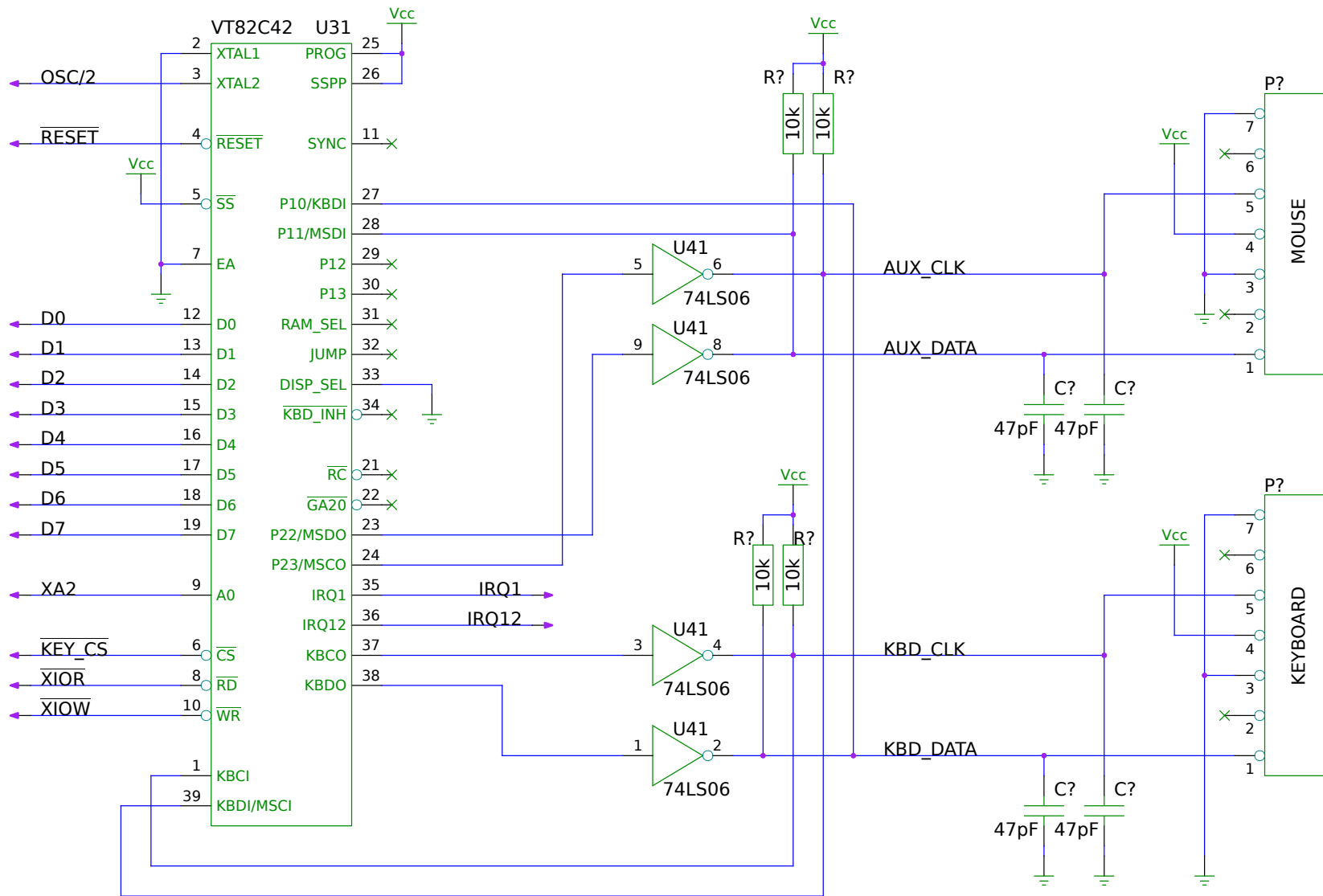
FILE: BB-88_page09.sch

DATE: 26 apr 2021

REVISION: 1.0

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BB-88: Keyboard Controller

TITLE

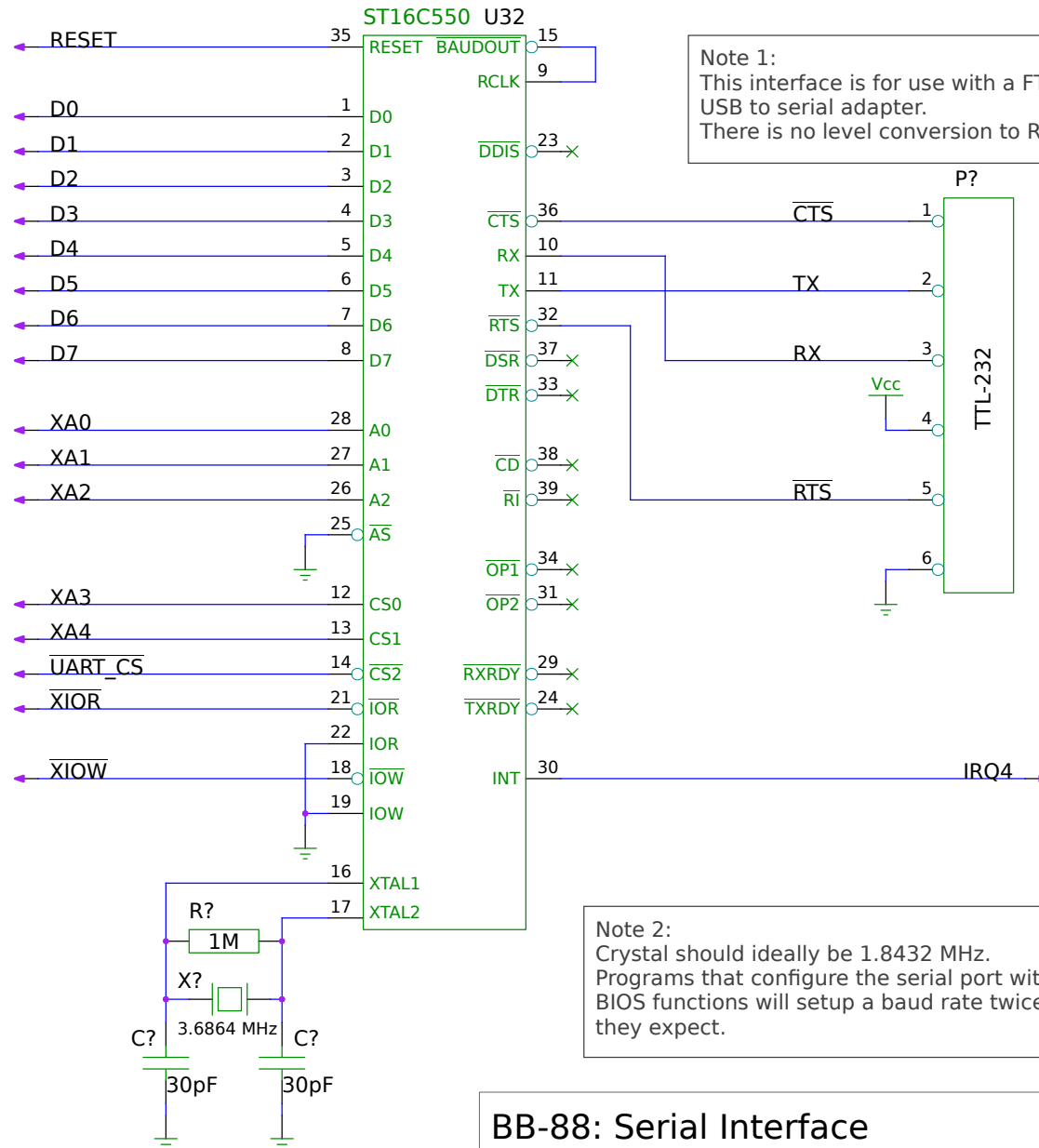
FILE: BB-88_page10.sch

DATE: 26 apr 2021

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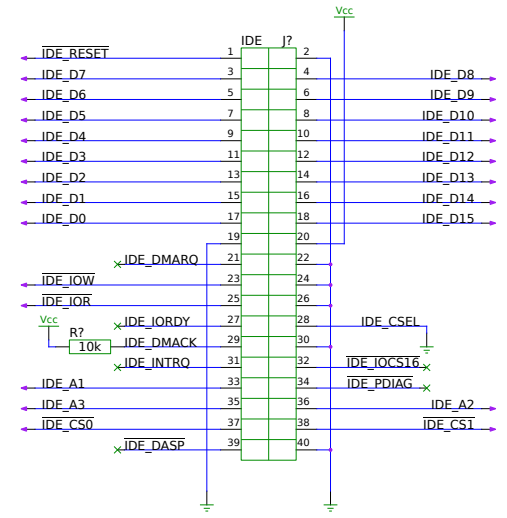
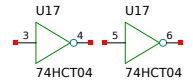
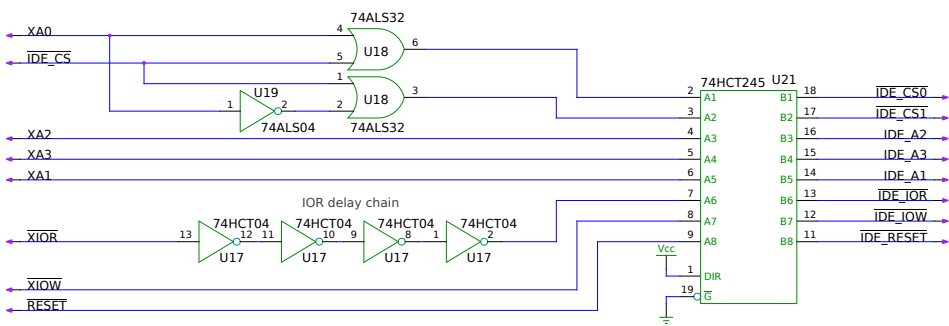
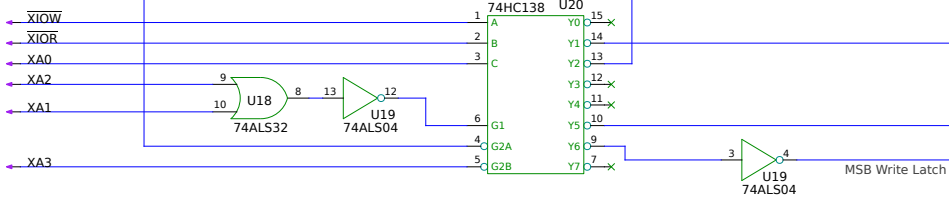
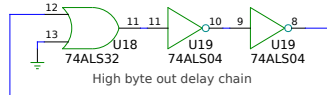
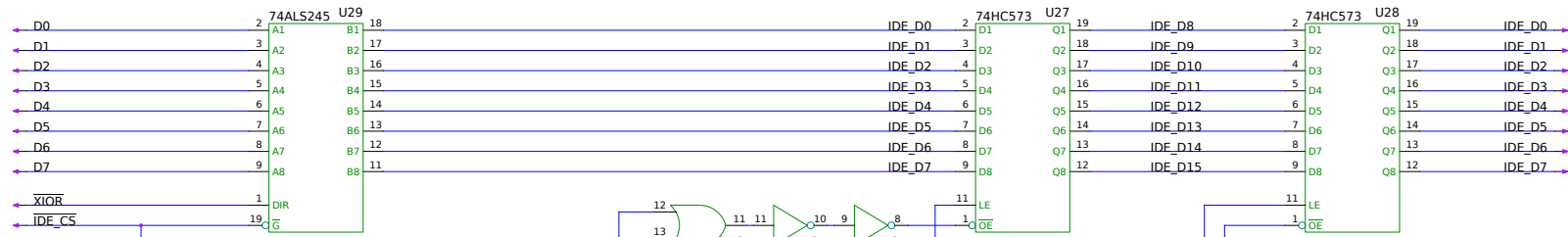
DRAWN BY: Amand Tihon



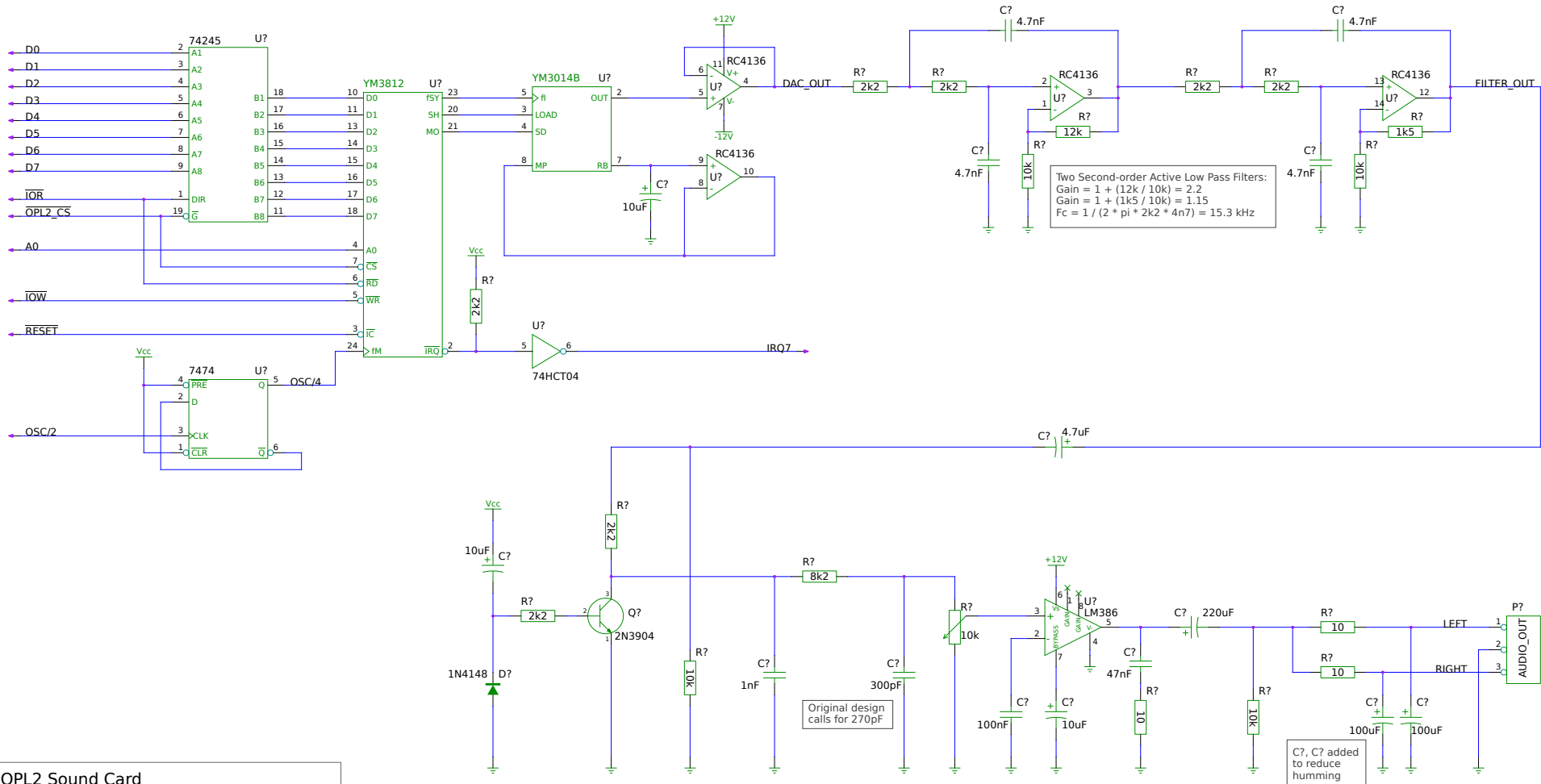
Note 1:
 This interface is for use with a FTDI TTL-232
 USB to serial adapter.
 There is no level conversion to RS-232.

Note 2:
 Crystal should ideally be 1.8432 MHz.
 Programs that configure the serial port without using the
 BIOS functions will setup a baud rate twice as high as what
 they expect.

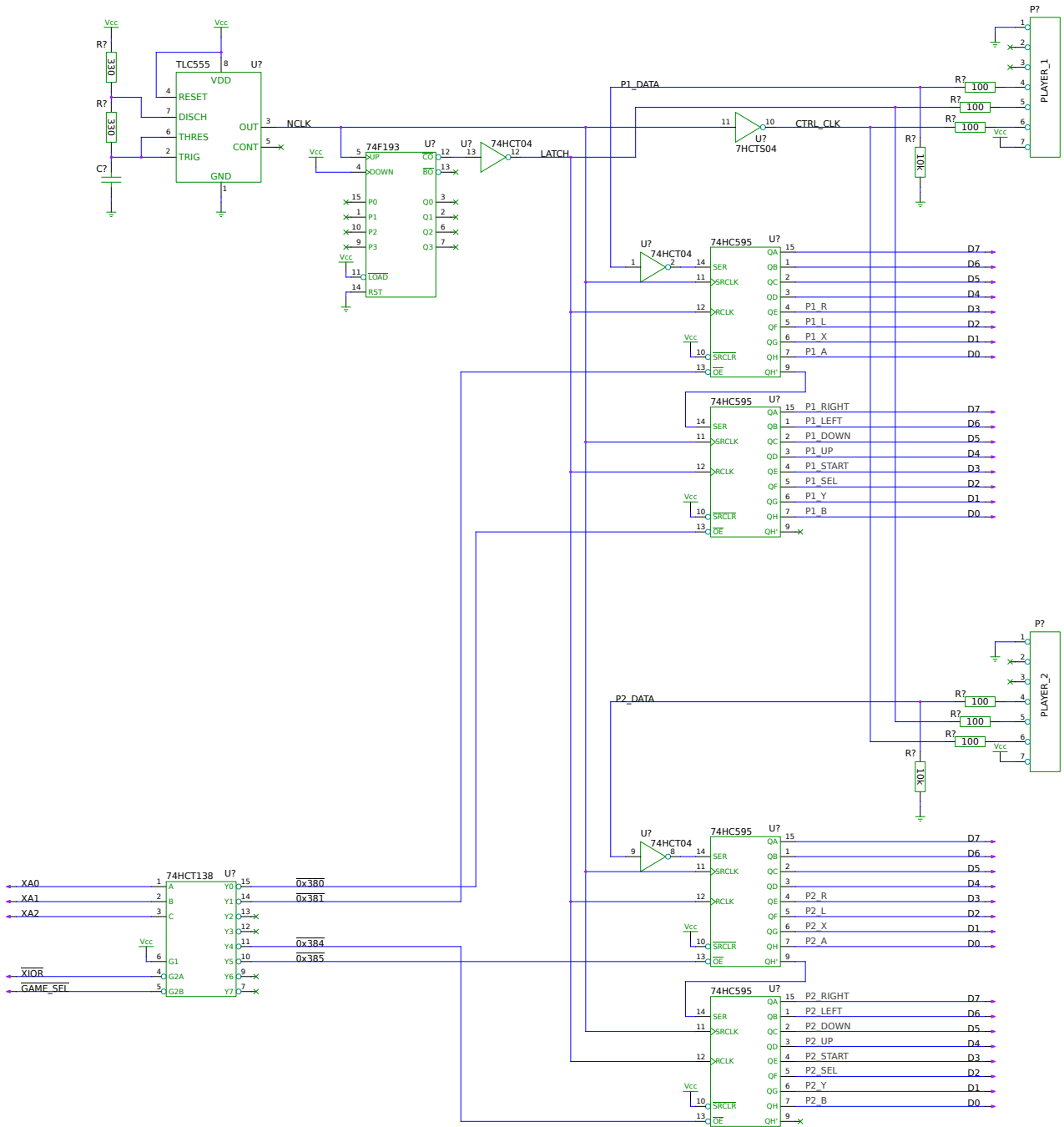
BB-88: Serial Interface		
TITLE		
FILE: BB-88_page11.sch	DATE: 27 apr 2021	REVISION: 1.0
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BB-88: IDE Adapter		
TITLE		
FILE: BB-88_page12.sch	DATE: 26 apr 2021	REVISION: 1.0
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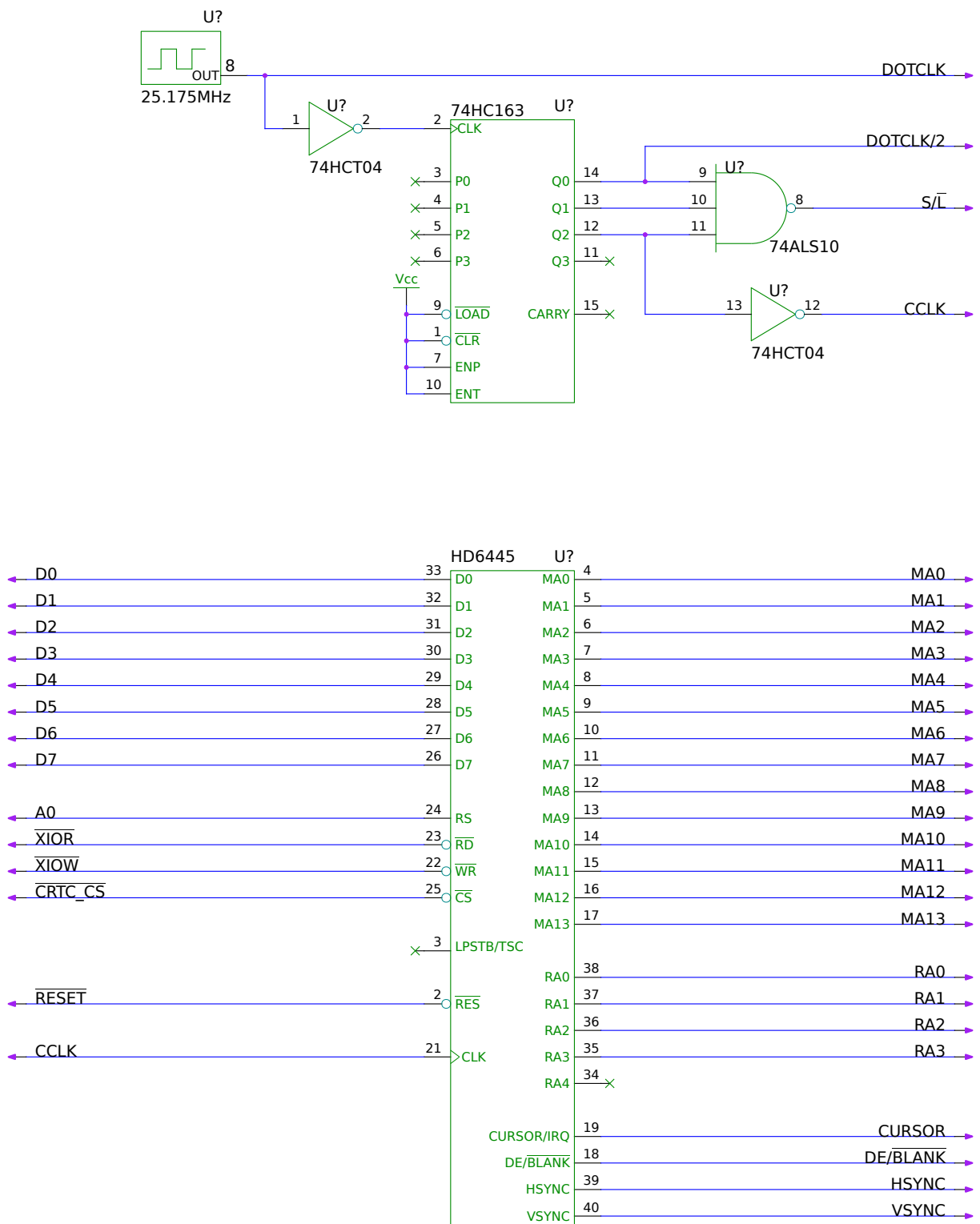


BB-88: OPL2 Sound Card		
TITLE		
FILE: BB-88_page13.sch	DATE: 29 apr 2021	REVISION: 1.0
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BB-88: SNES Controllers Interface

TITLE
 FILE: BB-88_page14.sch DATE: 04 may 2021 REVISION: 1.0
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BB-88: VGA - Timing generator, CRTC

TITLE

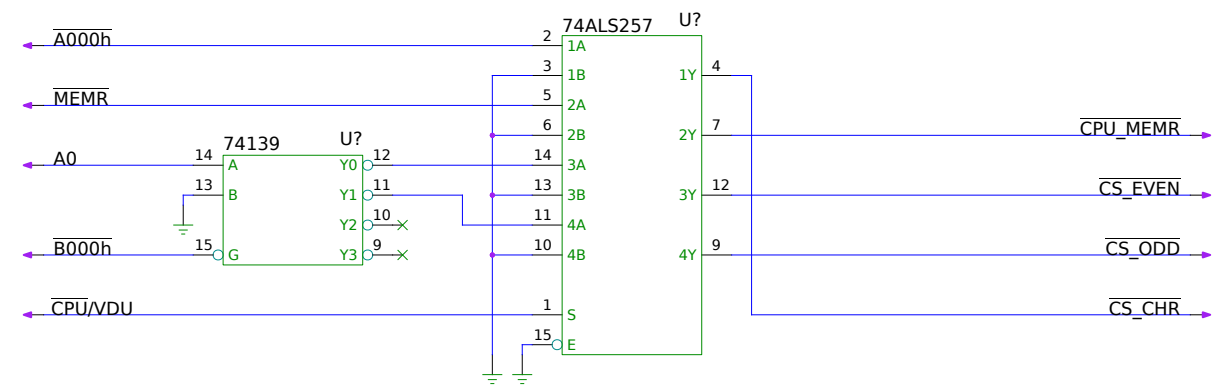
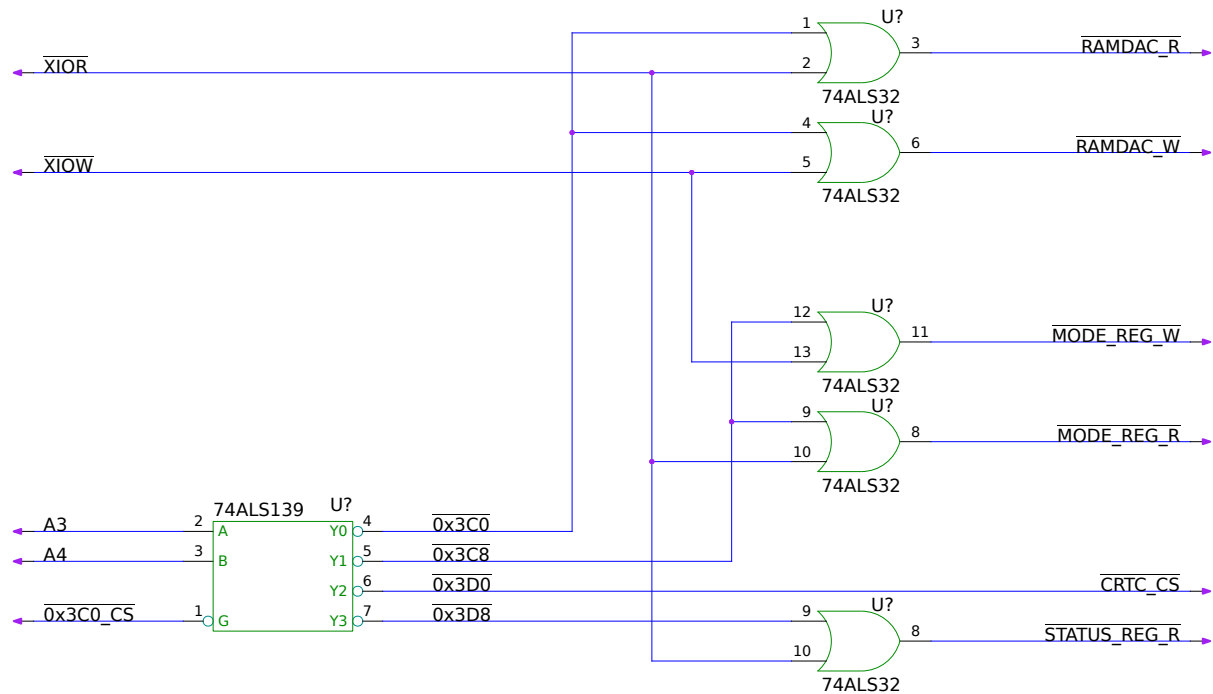
FILE: BB-88_vga01.sch

DATE: 6 may 2021

REVISION: 1.0

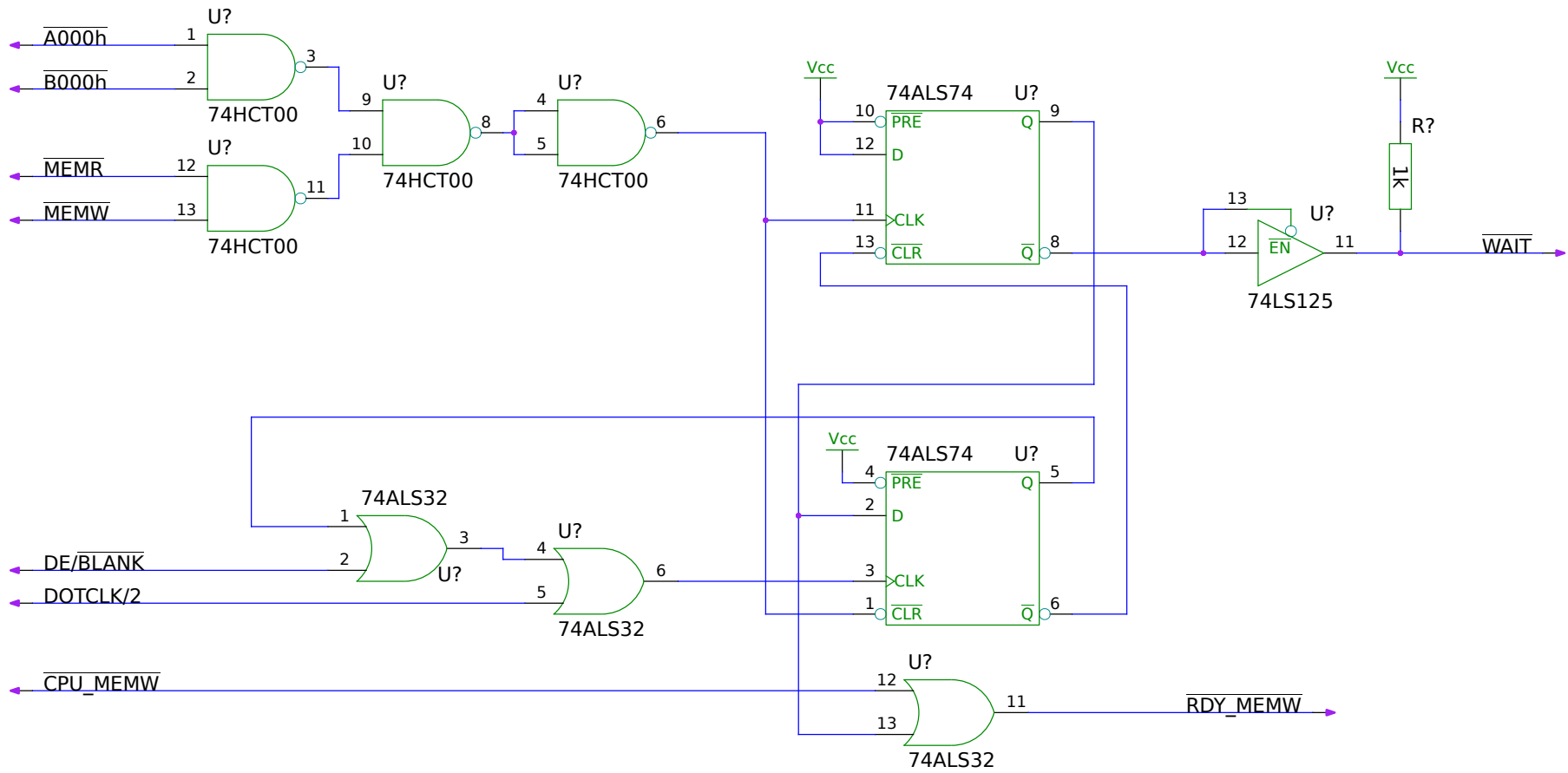
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BB-88: VGA - Address selectors

TITLE		
FILE: BB-88_vga02.sch	DATE: 6 may2021	REVISION: 1.0
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BB-88: VGA - Wait-States Generator

TITLE

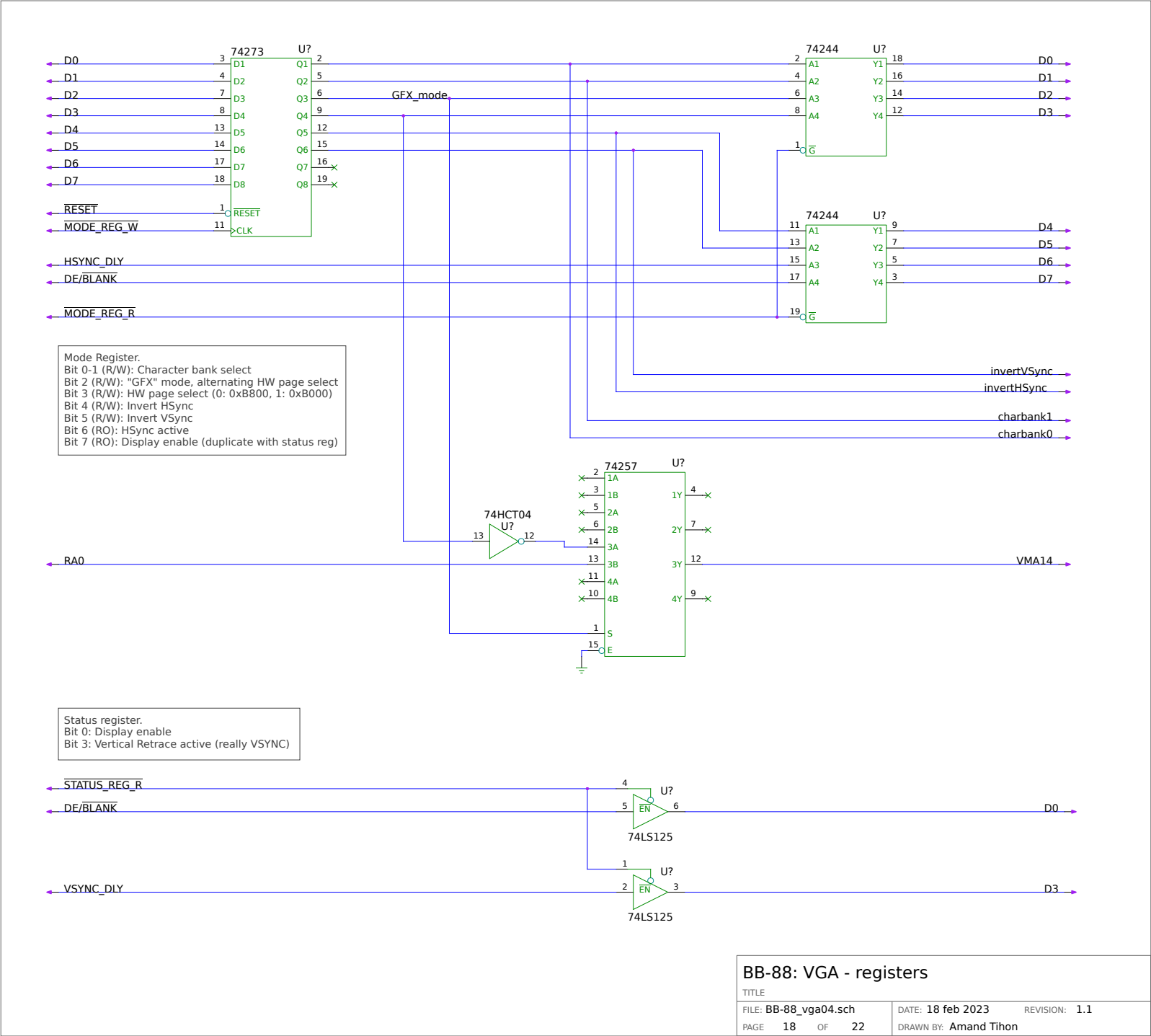
FILE: BB-88_vga03.sch

DATE: 7 may 2021

REVISION: 1.0

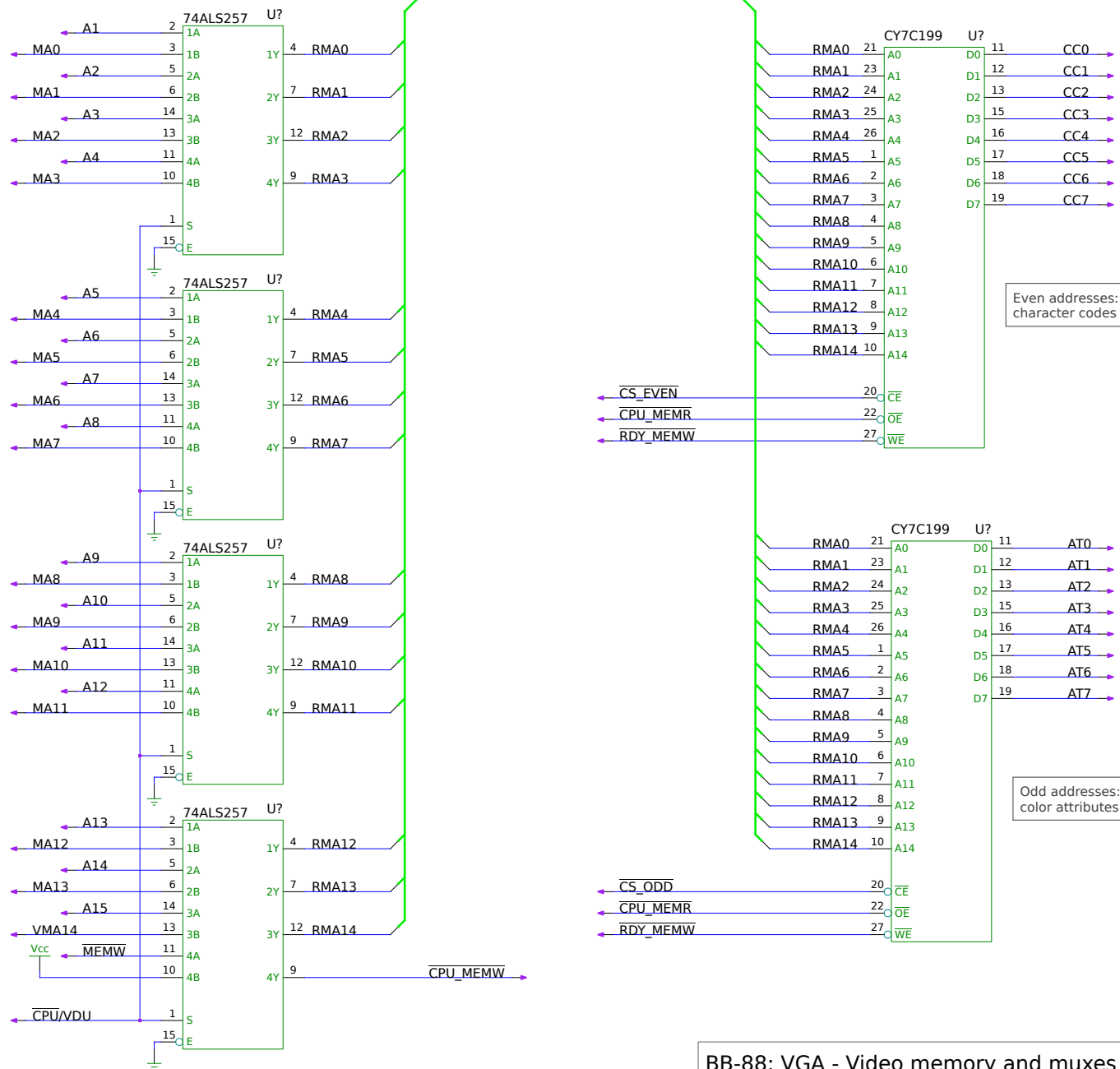
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BB-88: VGA - registers

TITLE		
FILE: BB-88_vga04.sch	DATE: 18 feb 2023	REVISION: 1.1
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BB-88: VGA - Video memory and muxes

TITLE

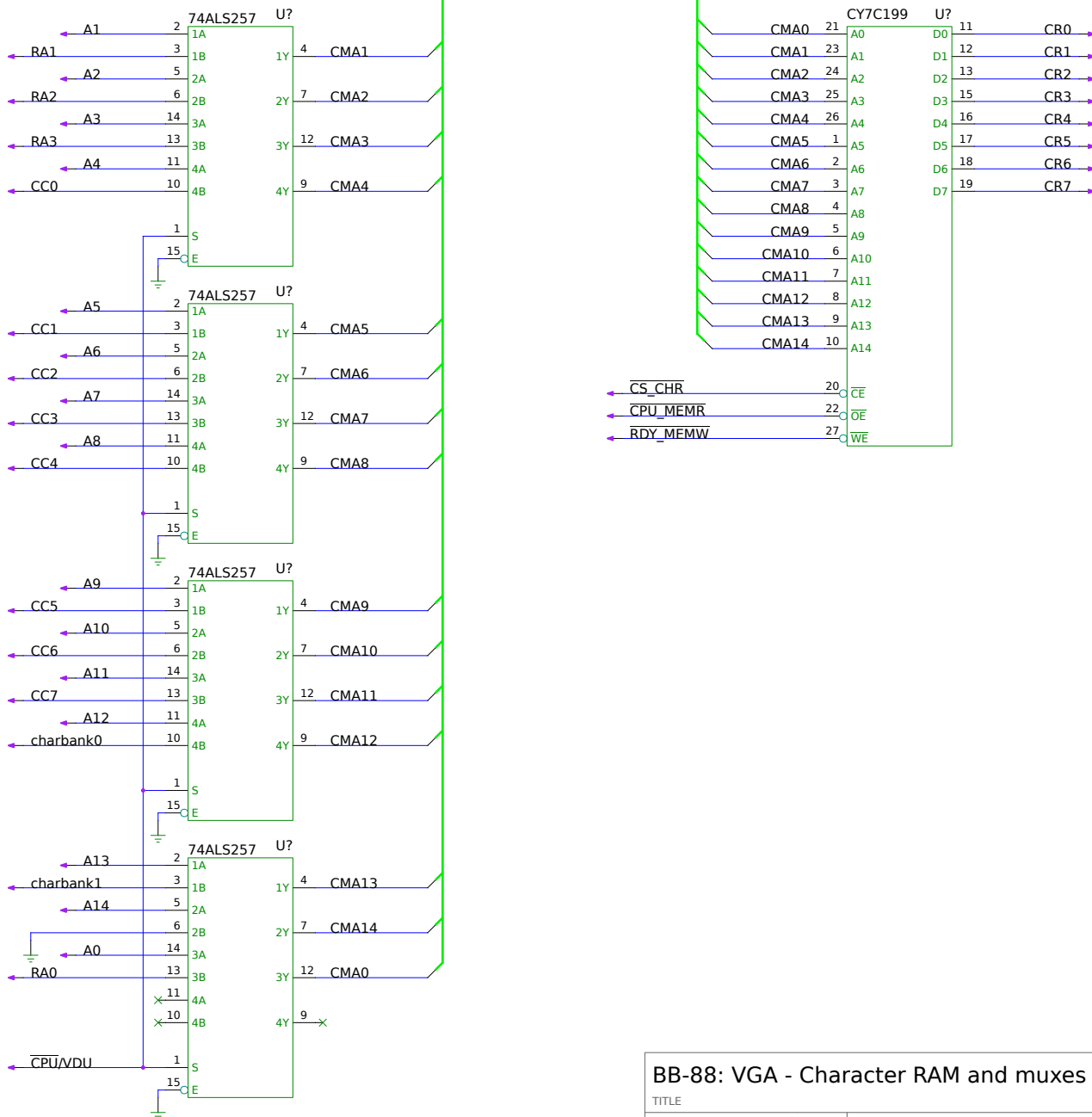
FILE: BB-88_vga05.sch

DATE: 6 may 2021

REVISION: 1.0

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BB-88: VGA - Character RAM and muxes

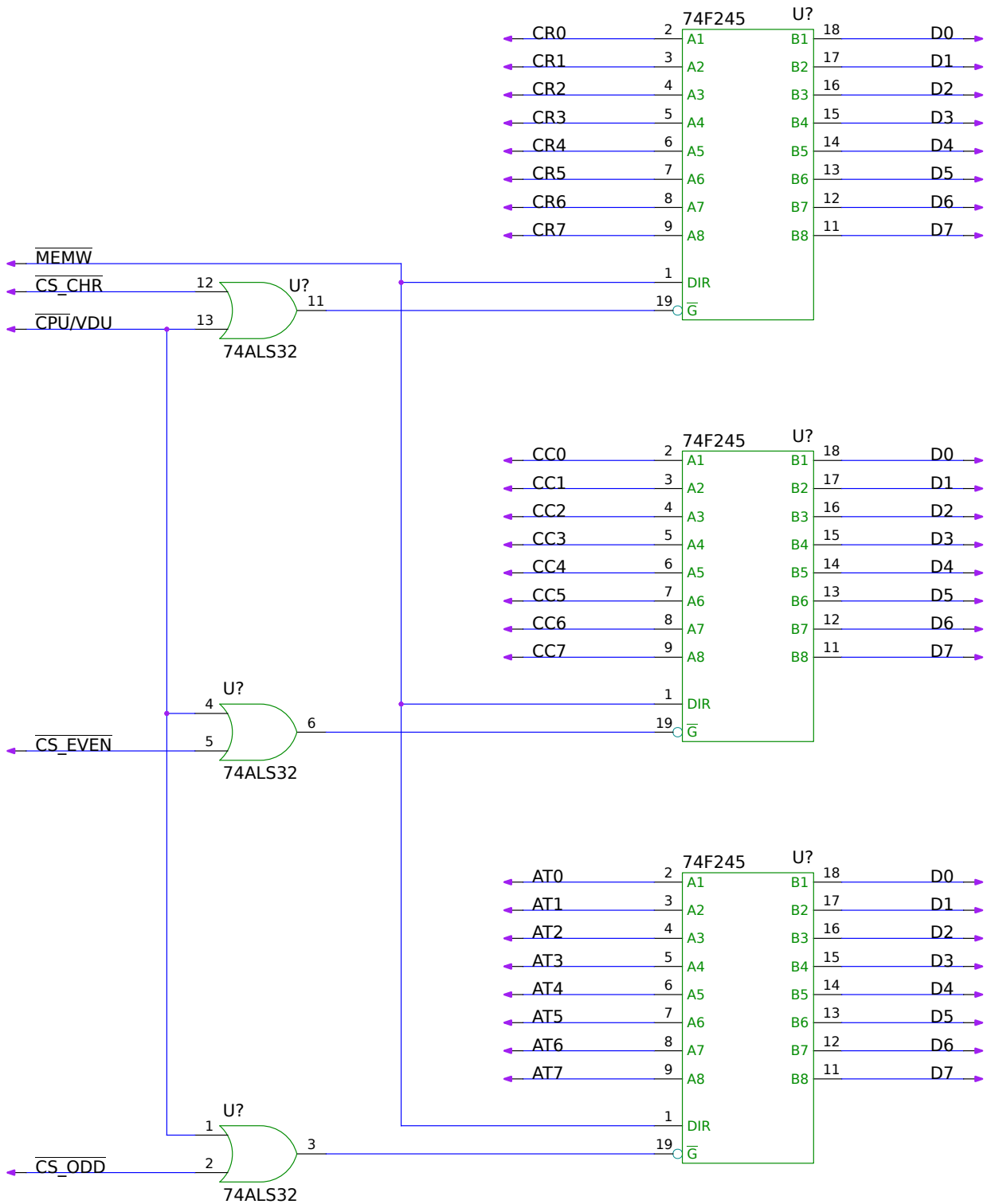
TITLE

FILE: BB-88_vga06.sch

DATE: 6 may 2021 REVISION: 1.0

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BB-88: VGA - Memory interface

TITLE

FILE: BB-88_vga07.sch

DATE: 6 may 2021

REVISION: 1.0

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